

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions, and listings, of claims in the application.

1. (Previously Presented) A printed circuit board comprising:
external surfaces and a plurality of internal-layer signal lines traced within an internal layer of the printed circuit board between the external surfaces; and
a plurality of outermost-layer signal lines traced on one of the external surfaces of the printed circuit board and connected with respective internal-layer signal lines through via holes, wherein the outermost-layer signal lines are each connected with respective connector lines of a connector mounted on one of the external surfaces of the printed circuit board, and total lengths of respective outermost-layer signal lines and connector lines of the connector connecting corresponding outermost-layer signal lines to respective connected via holes are substantially equal.

2. (Previously Presented) The printed circuit board according to Claim 1, wherein the outermost-layer signal lines are each straight and are free of bends.

Claim 3 (Cancelled).

4. (New) The printed circuit board according to Claim 1, wherein, when total lengths of the connector lines and total lengths of the outermost signal lines are not substantially equal, the total lengths are made substantially equal

5. (New) A printed circuit board arrangement comprising:
a printed circuit board including external surfaces, a plurality of internal-layer signal lines traced within an internal layer of the printed circuit board between the

external surfaces, and a plurality of outermost-layer signal lines traced on one of the external surfaces of the printed circuit board and connected with respective internal-layer signal lines through via holes; and

a connector mounted on one of the external surfaces of the printed circuit board and including a plurality of connector lines, wherein the outermost-layer signal lines are each connected with respective connector lines of the connector mounted on one of the external surfaces of the printed circuit board, and total lengths of respective outermost-layer signal lines and the connector lines of the connector connecting corresponding outermost-layer signal lines to respective connected via holes are substantially equal.

6. (New) The printed circuit board assembly according to Claim 1, wherein the outermost-layer signal lines are each straight and are free of bends.